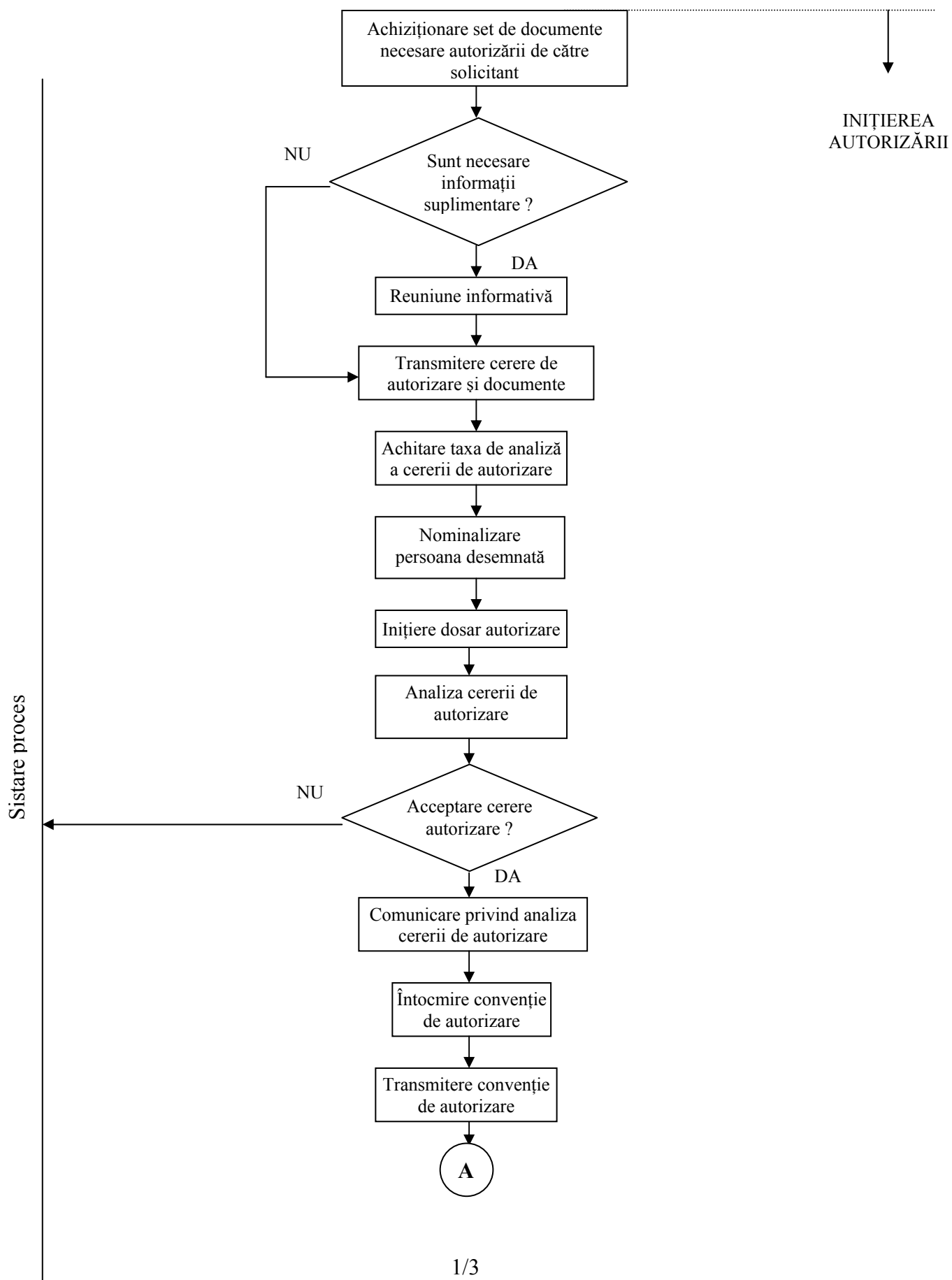


SCHEMA LOGICĂ A PROCESULUI DE AUTORIZARE



Sistare proces

